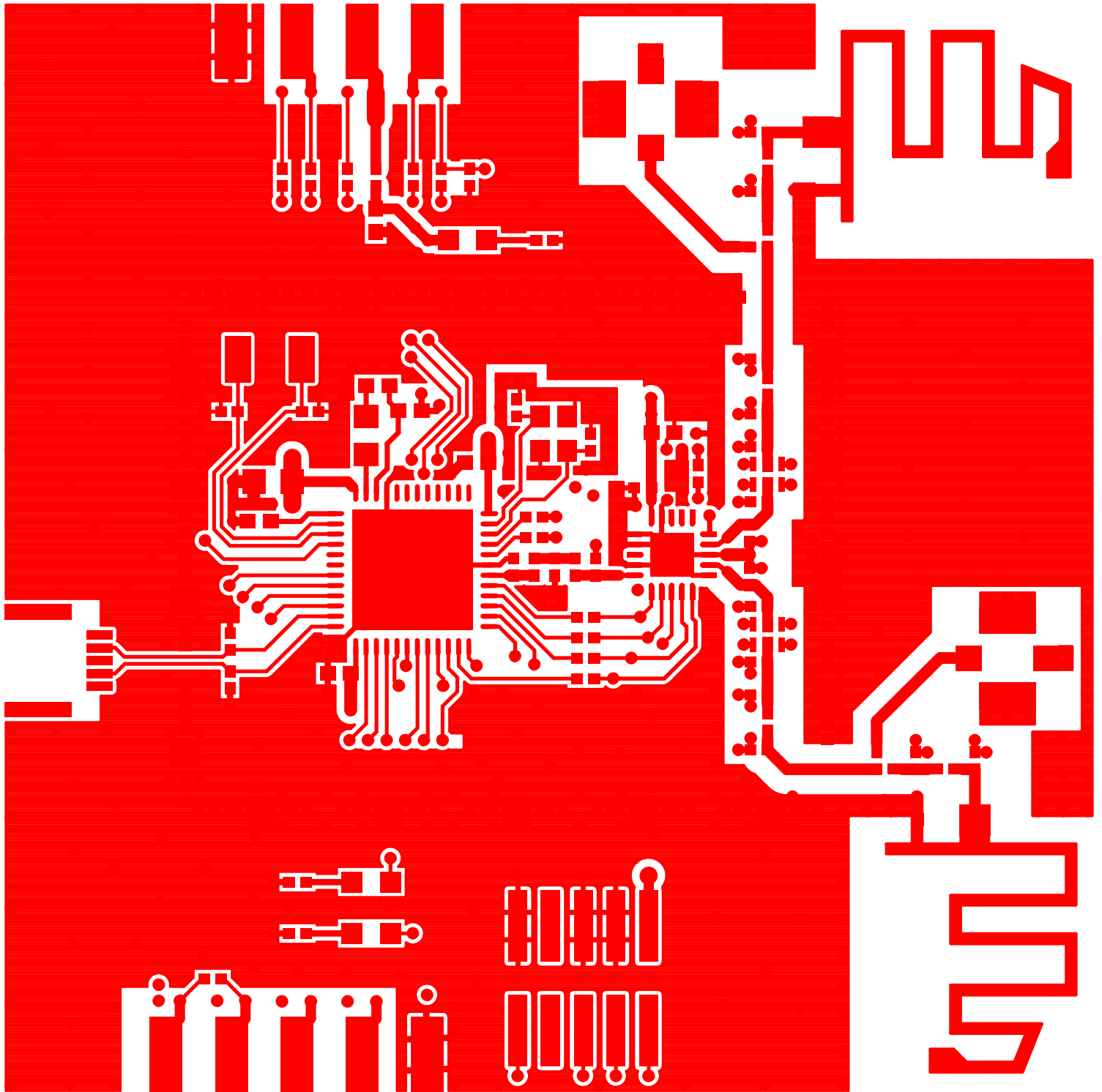
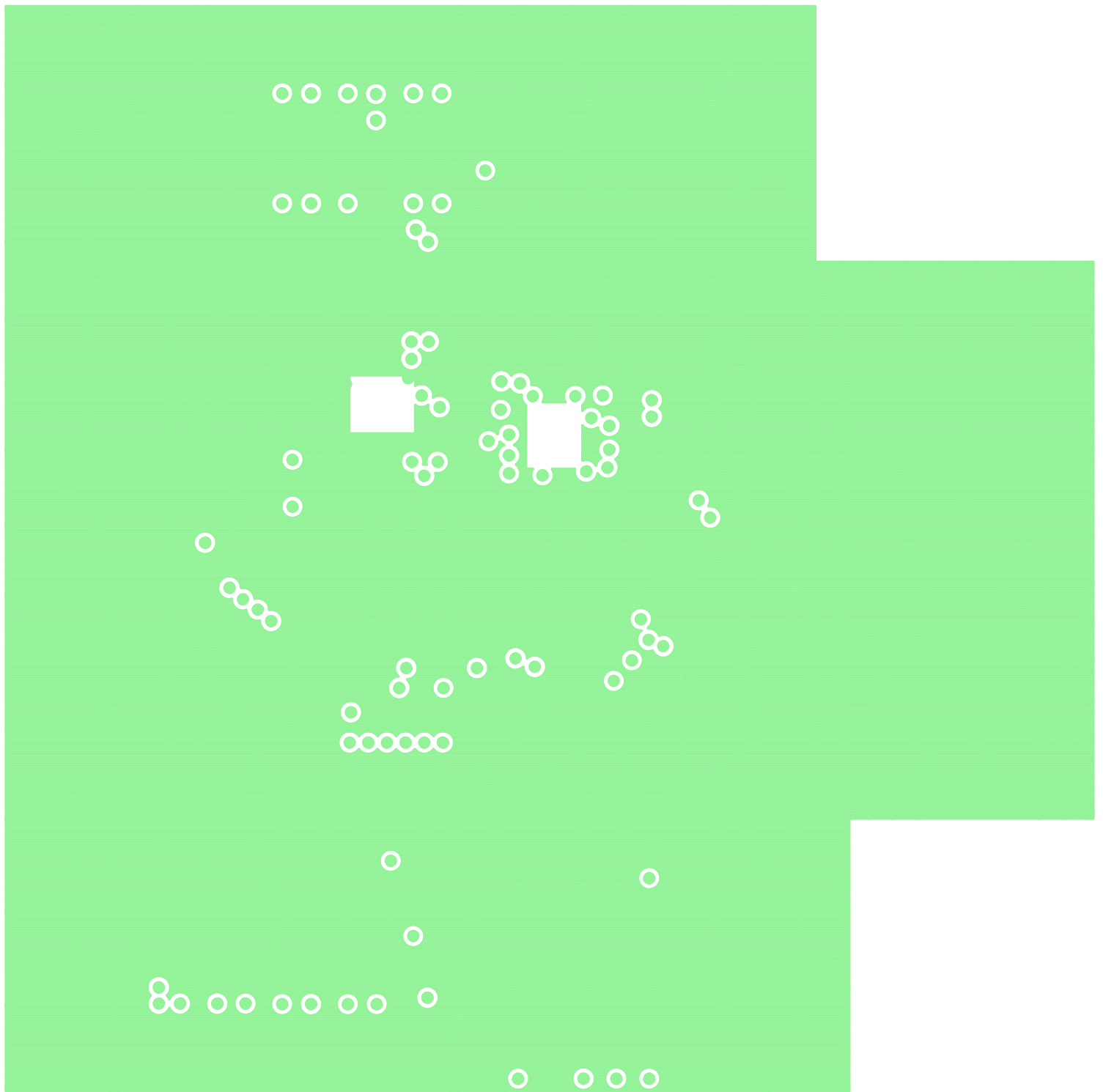


RD-nRF52-RFX2411N-REV2

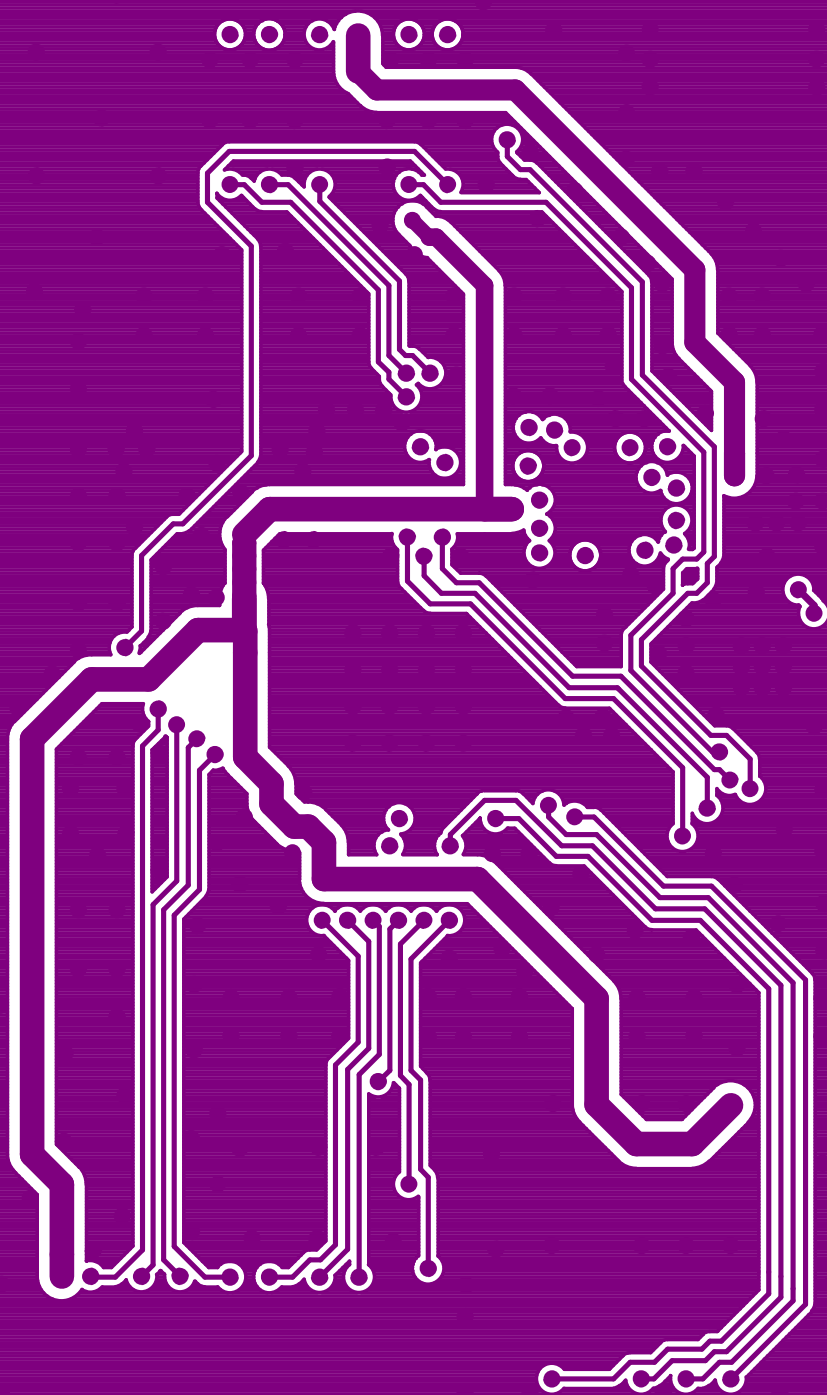
BOARD OUTLINE



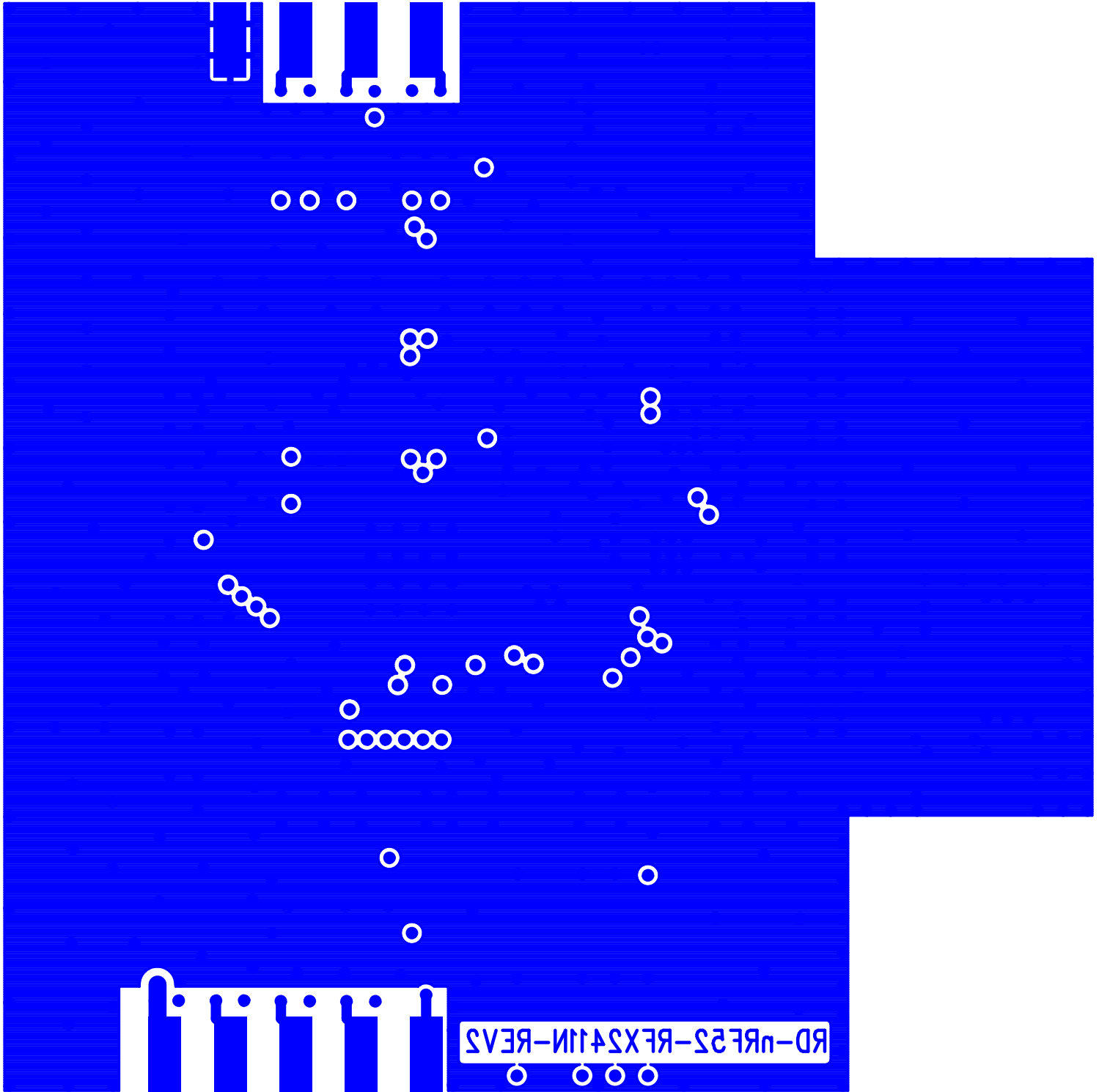
RD-nRF52-RFX2411N-REV2
LAYER 1



RD-nRF52-RFX2411N-REV2
LAYER 2



RD-nRF52-RFX2411N-REV2
LAYER 3

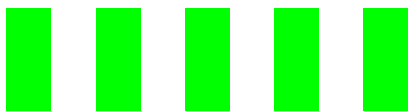


RD-nRF52-RFX2411N-REV2
LAYER 4

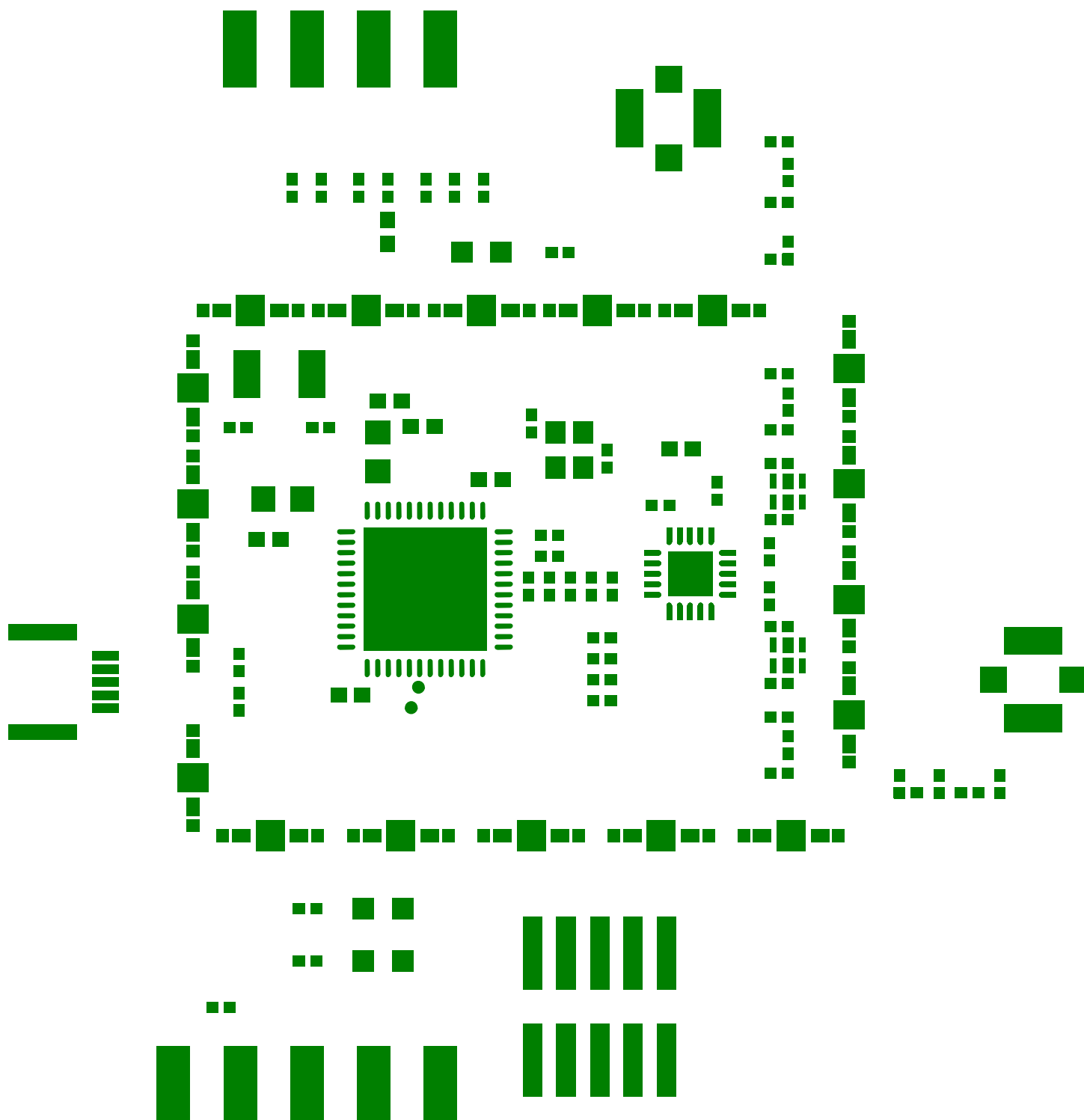
RD-nRF52-RFX2411N-REV2
PASTEMASK BOTTOM



RD-nRF52-RFX2411N-REV2
PASTEMASK TOP



RD-nRF52-RFX2411N-REV2
SOLDERMASK BOTTOM



RD-nRF52-RFX2411N-REV2
SOLDERMASK TOP

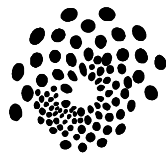
1
BDEL
RXENR
2WVNLB
GND

1
ADD
B0.0E
B0.08
B0.15
B0.1E

RD-nRF52-RFX2411N-REV2
SILKSCREEN BOTTOM

RD-nRF52-RFX2411N
nRF52 + RFX2411N FEM

NFC



RFaxis
BRIDGING THE RF GAP

P0.05 2

P0.07

P0.11

P0.13

GND



J1



J2

3 GND

MODER

PA VDD

TXENR

2

R14

R13

R11

R6

R10

R1

R17

C12

D3

R4

J4

C29

L11

C30

R20

R5



L1

C1

C34

C33

C2

C5

L2

U1

C3

C4

C6

C7

C8

C18

C19

C17

L5

C16

C15

FL1

C14

L

U2

FL2

C22

C23

L8

C24

C20

C13

C21

L7

L

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L3

C10

R18

C27

R8

R9

R12

R15

C25

C26

C11

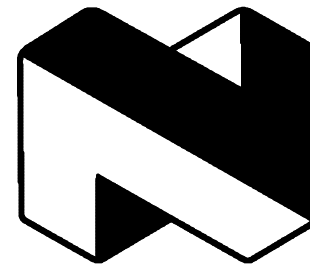
T1

T2

J8

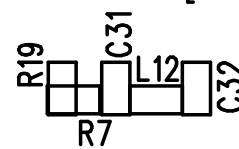
J7

WIRELESS BY



NORDIC
SEMICONDUCTOR

J7



A1

A2

RD-nRF52-RFX2411N-REV2
SILKSCREEN TOP

